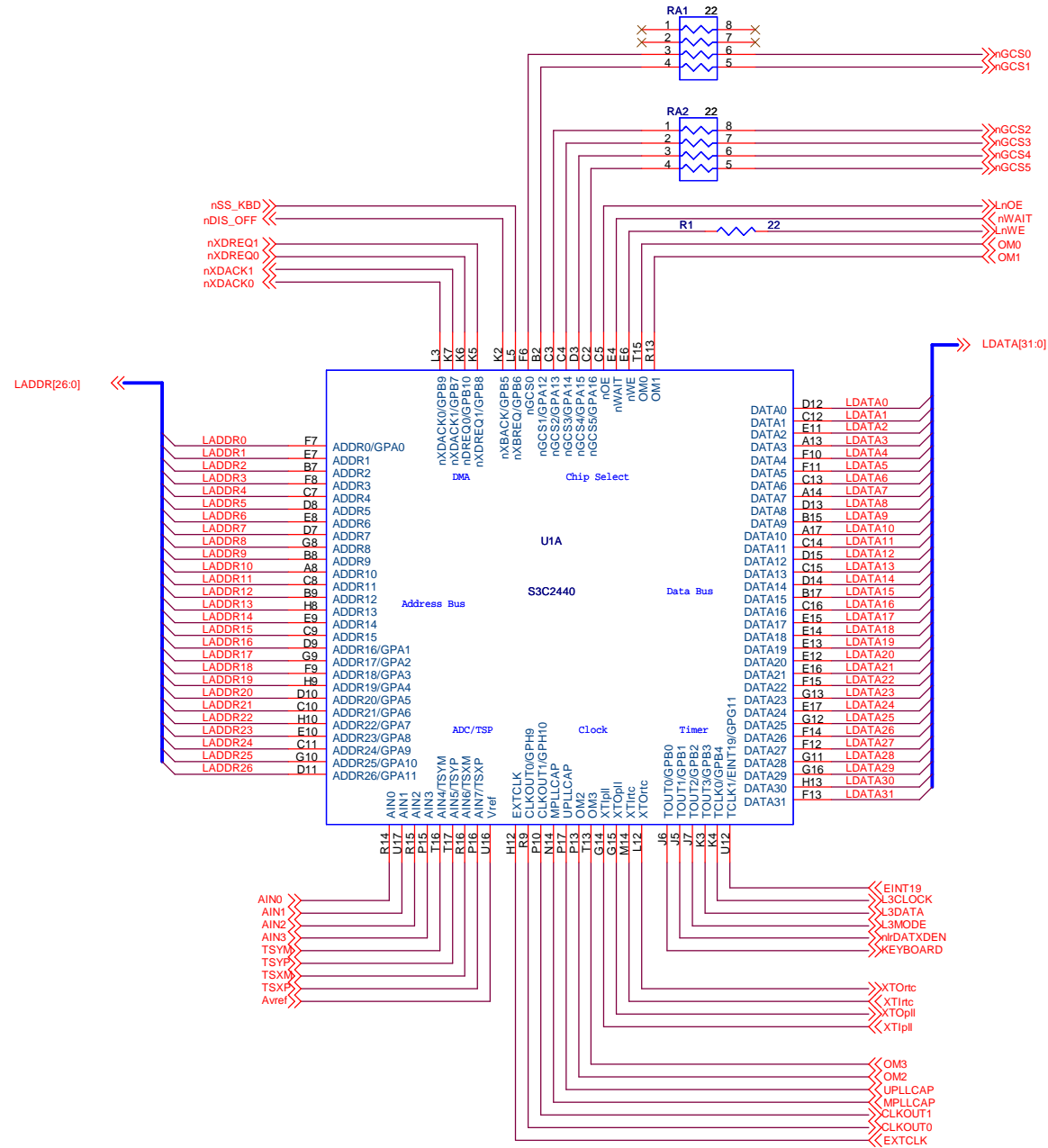
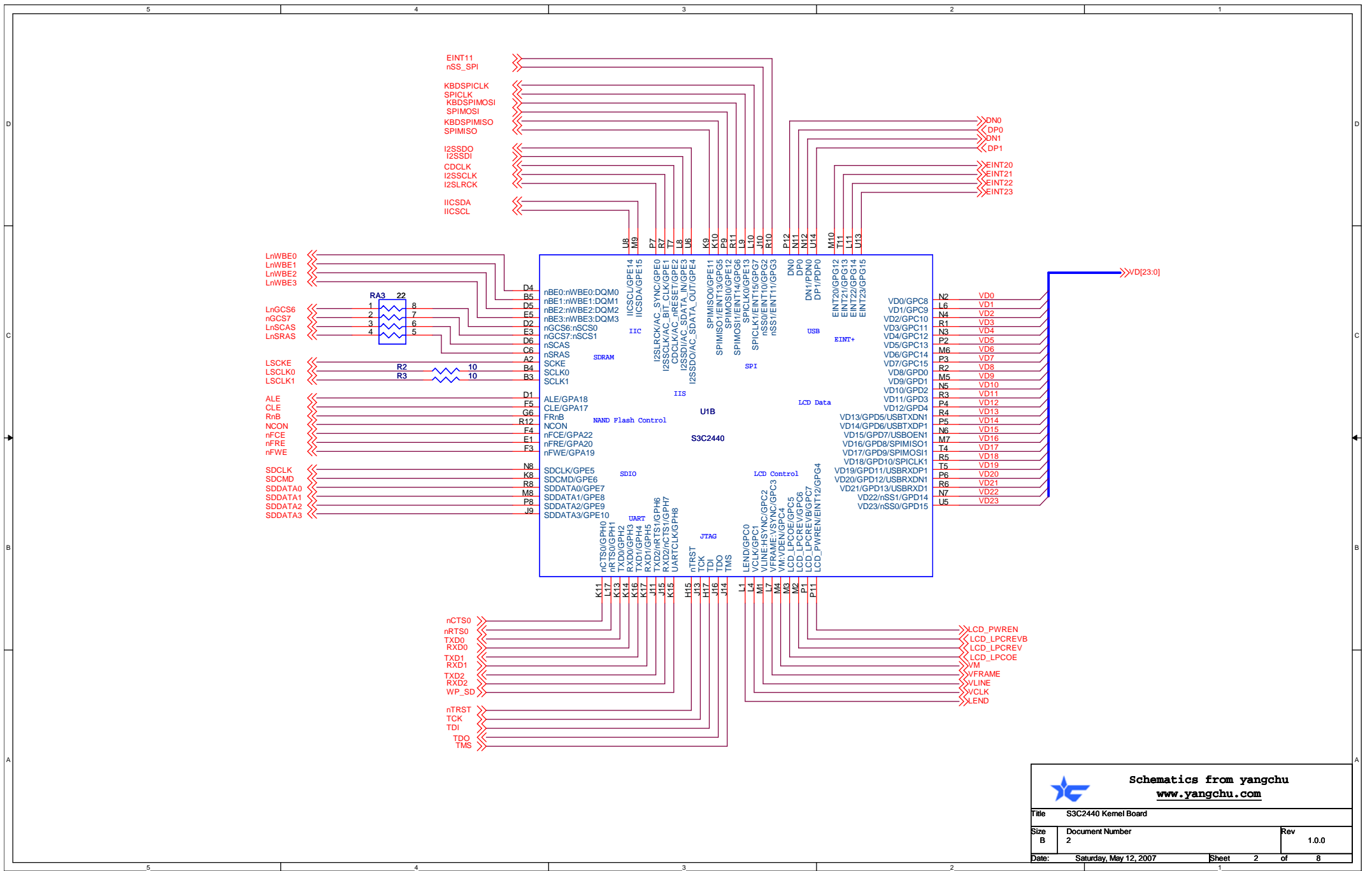


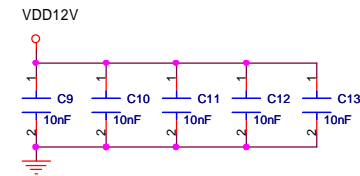
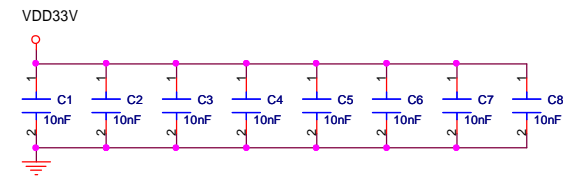
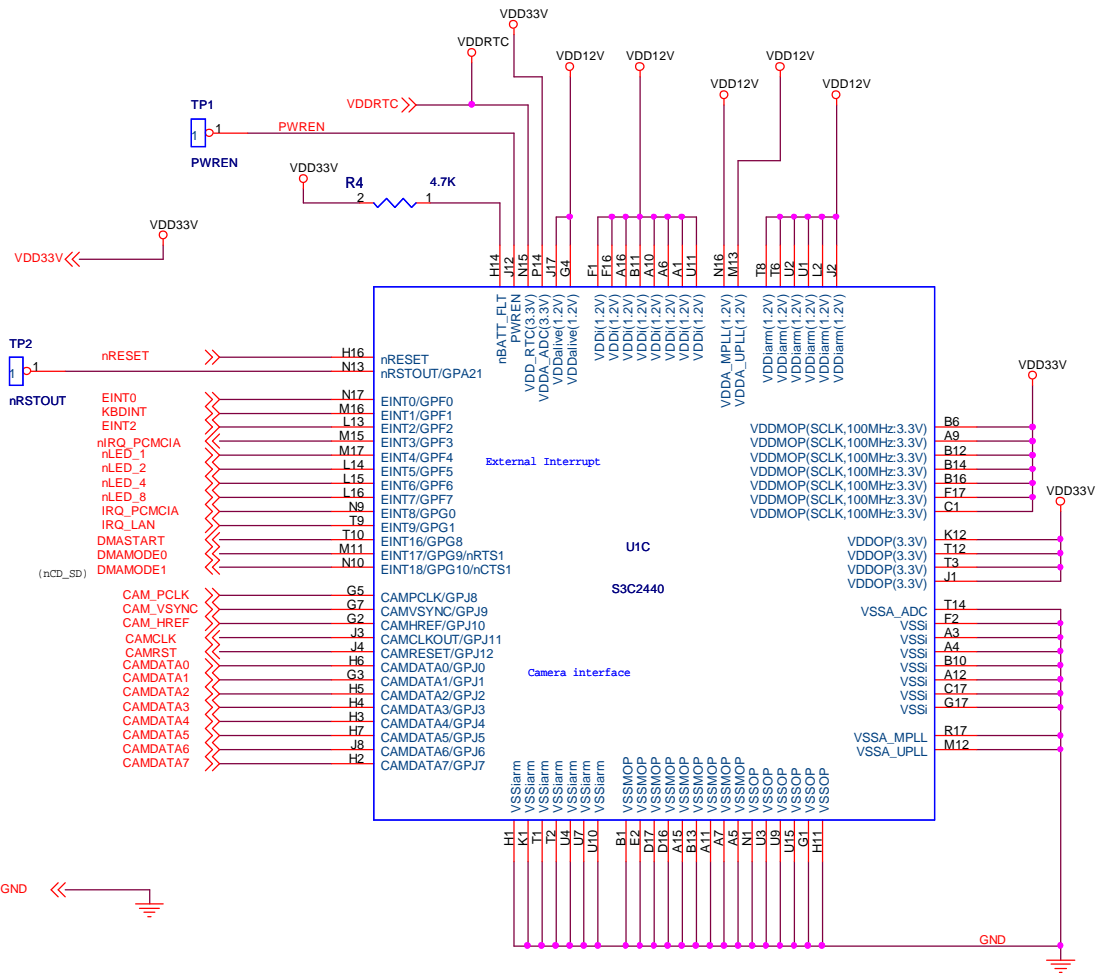
NOTE:
LXXXX signals should be routed as short as possible.
Insert pan nut to CPU socket guide hole.




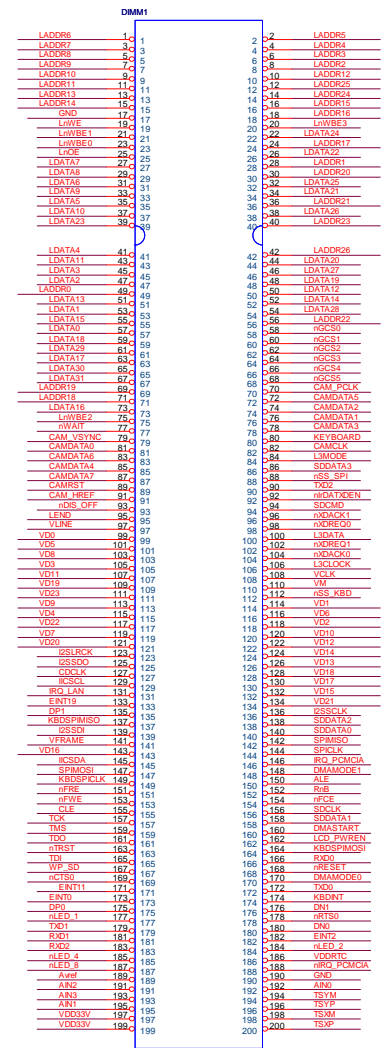
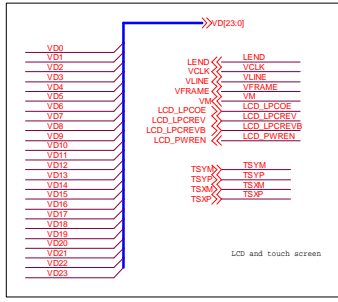
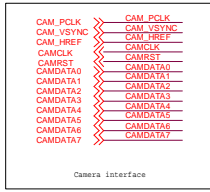
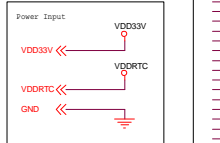
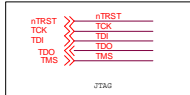
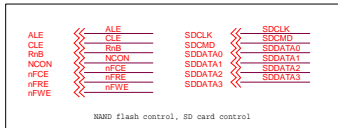
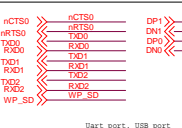
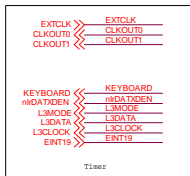
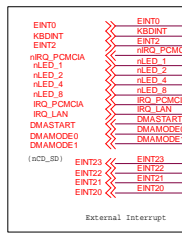
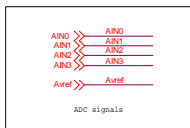
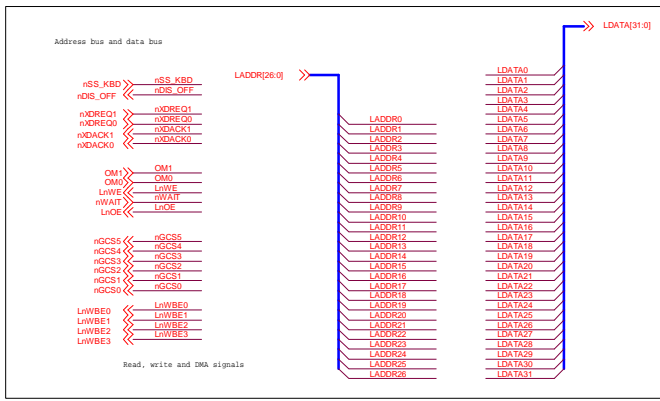
Schematics from yangchu
www.yangchu.com

Title: S3C2440 Kernel Board		
Size: B	Document Number: 1	Rev: 1.0.0
Date: Saturday, May 12, 2007	Sheet: 1	of: 1

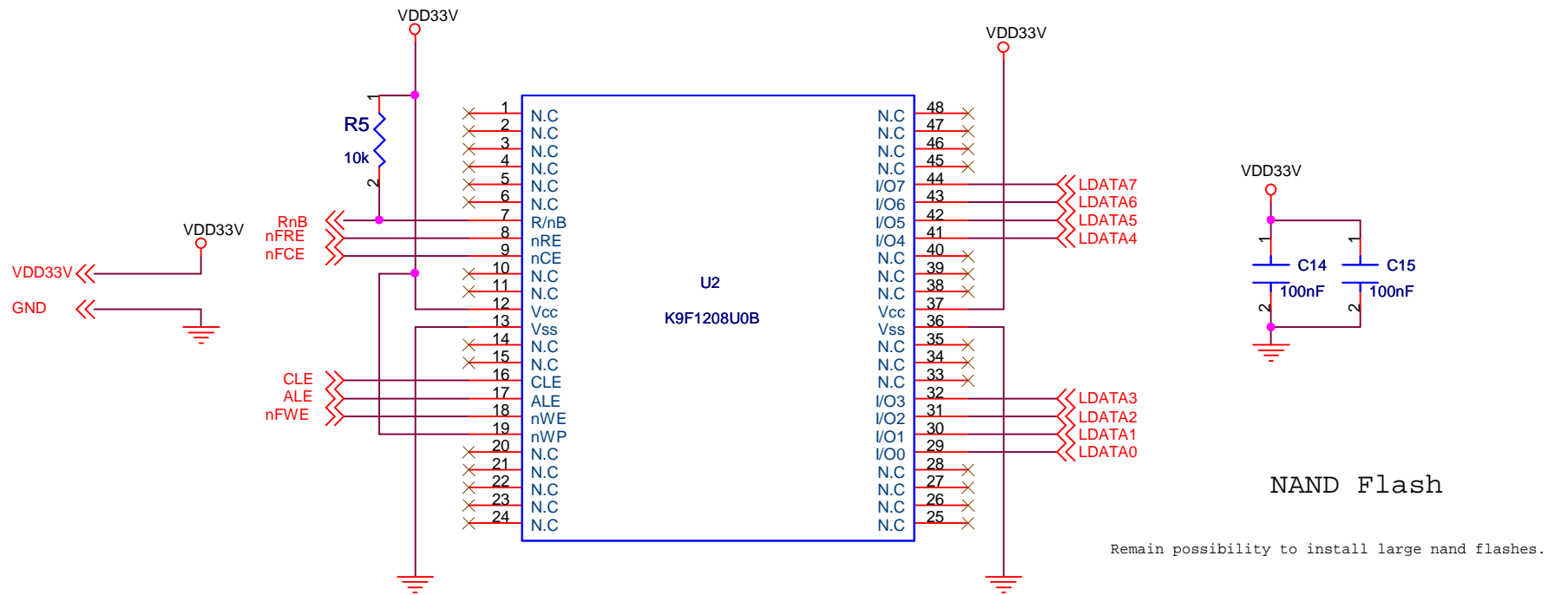




 Schematics from yangchu www.yangchu.com		
Title S3C2440 Kernel Board		
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DOR dian200

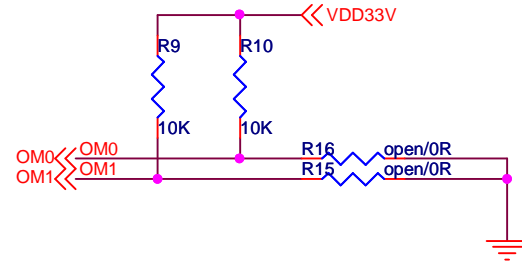
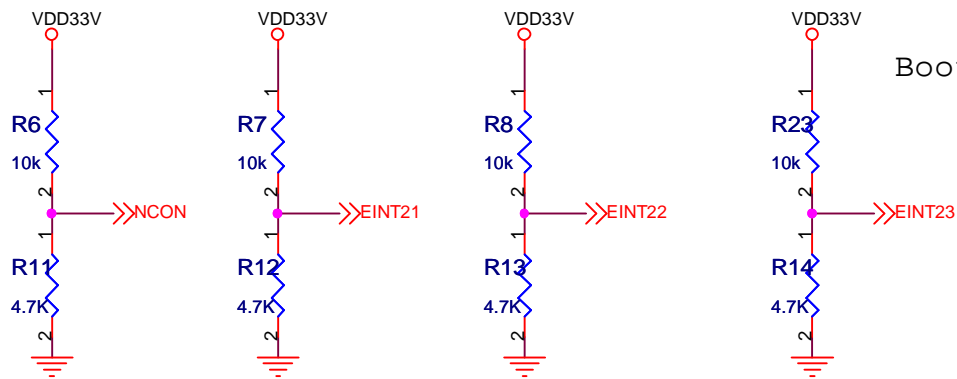


Remain possibility to install large nand flashes.

NAND Flash

Boot rom select

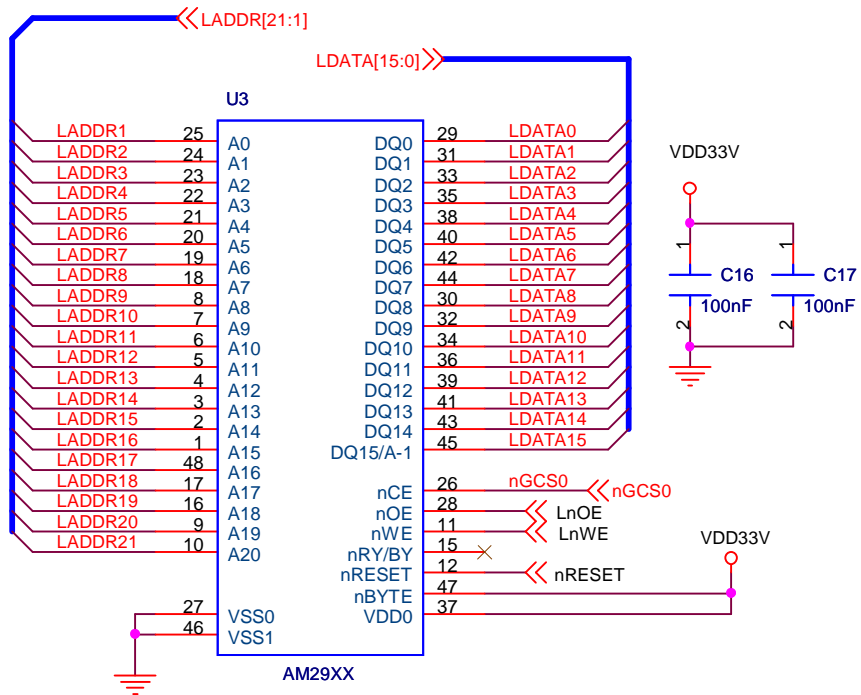
- OM1,OM0
- 00 nand boot
- 01 halfwordboot
- 10 full word boot
- 11 for test



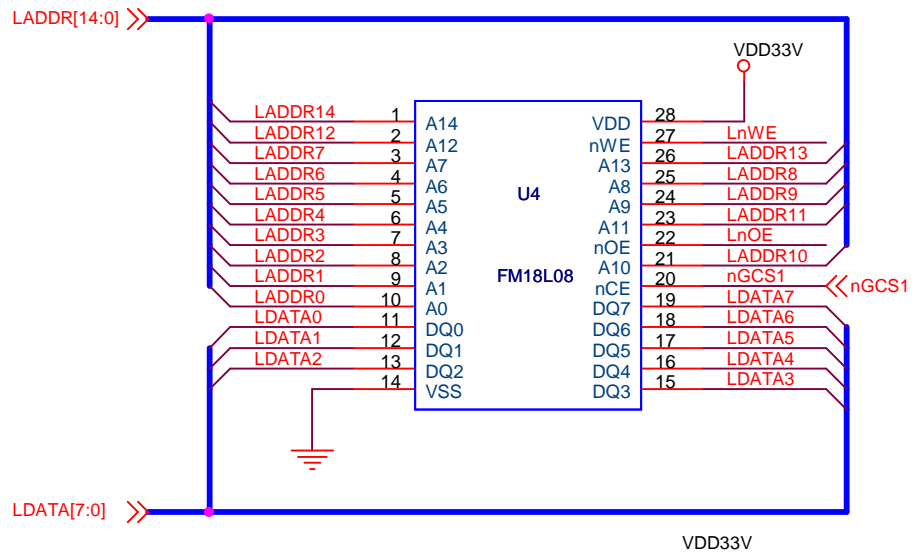
- | | | | |
|-------------------|----------------------------|------------------------------|---------------------|
| 0: Normal Flash | 0: Page=256Words(NCON = 0) | 0: 3 address cycle(NCON = 0) | 0: 8-bit bus width |
| 1: Advanced Flash | or Page=1KWords(NCON = 1) | or 4 address cycle(NCON = 1) | 1: 16-bit bus width |
| | 1: Page=512Bytes(NCON = 0) | 1: 4 address cycle(NCON = 0) | |
| | or Page=2KBytes(NCON = 1) | or 5 address cycle(NCON = 1) | |

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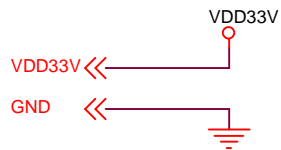
Title S3C2440 Kernel Board		
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AMD Flash

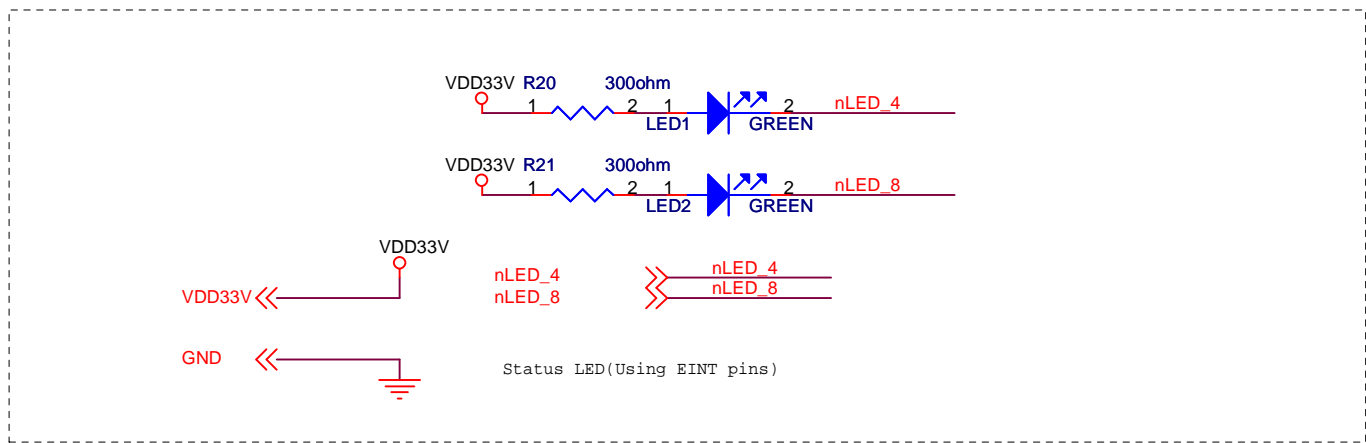
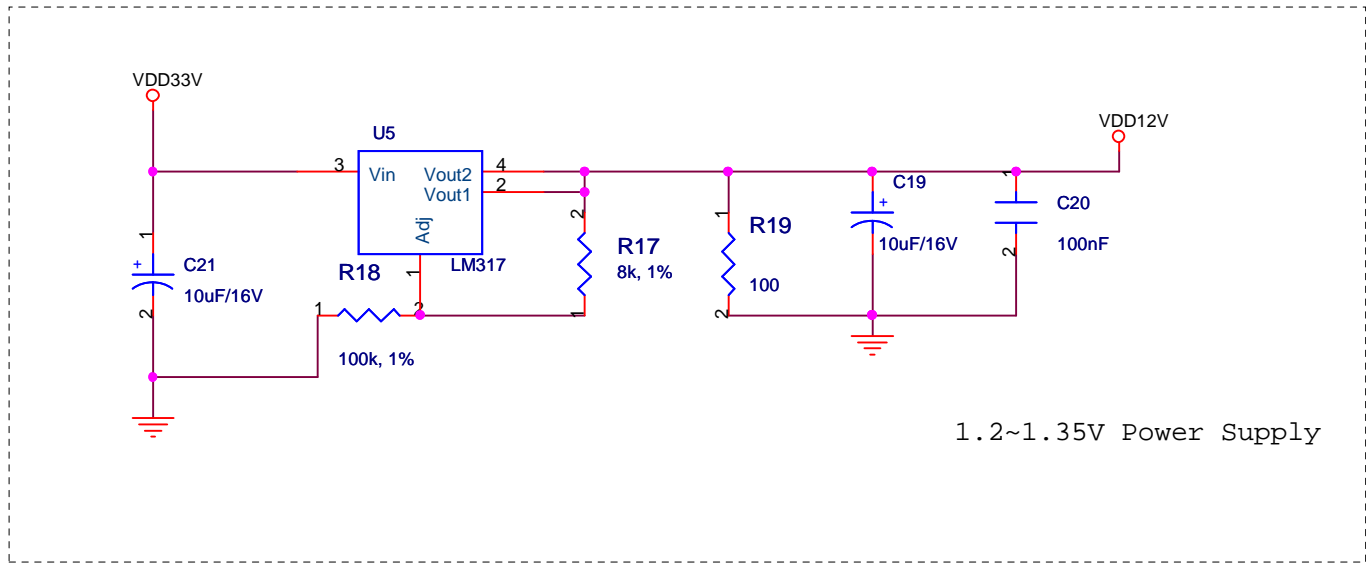



FRAM Memory

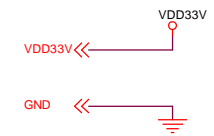
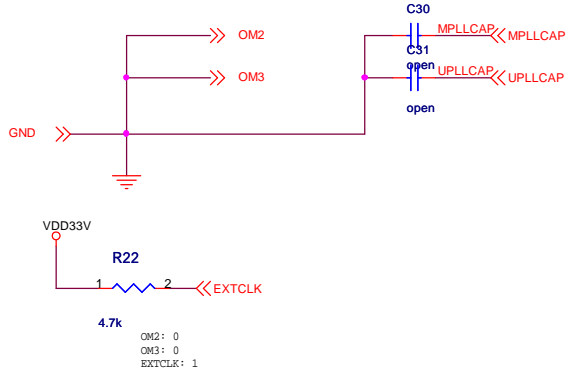
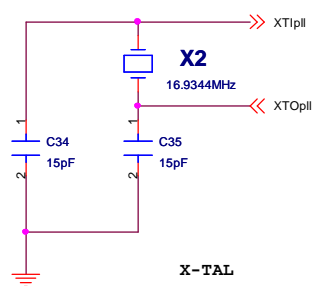
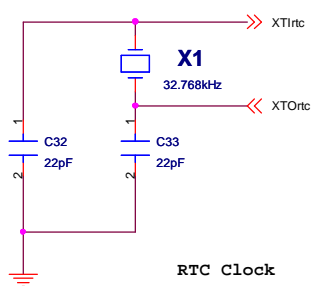
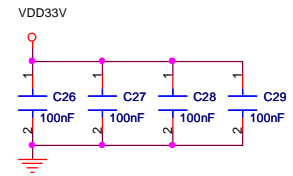
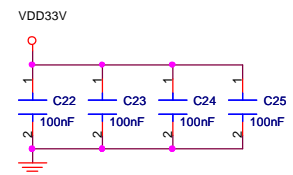
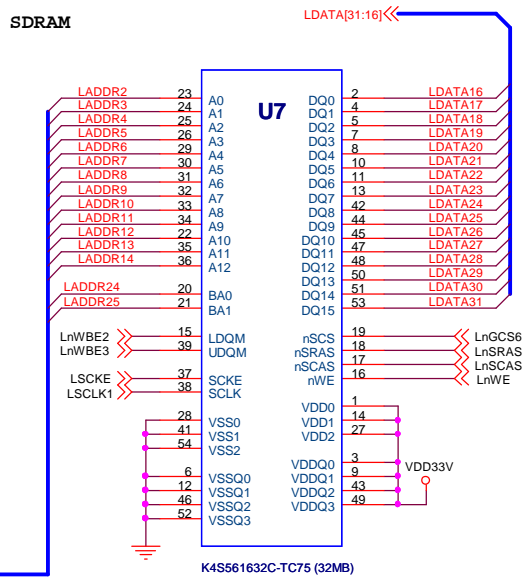
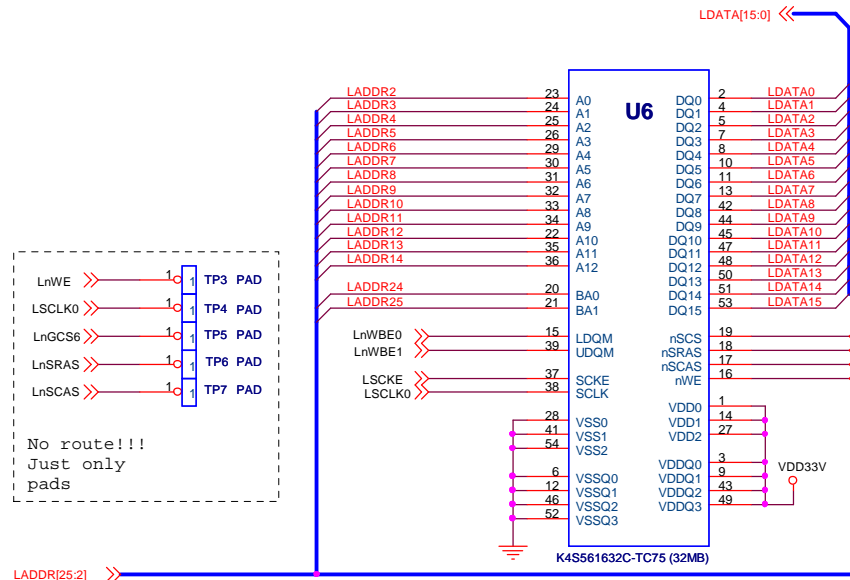



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		Title S3C2440 Kernel Board
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Title: SSC2440 Kernel Board		
Size: B	Document Number: 8	Rev: 1.0.0
Date: Saturday, May 12, 2007	Sheet: 8	of: 8